Electronic Patent Application Fee Transmittal							
Application Number:	10711493						
Filing Date:	21-Sep-2004						
Title of Invention:	A METHOD, SYSTEM, AND COMPUTER PROGRAM PRODUCT FOR GENERATING AND VERIFYING ISOLATION LOGIC MODULES IN DESIGN OF INTEGRATED CIRCUITS						
First Named Inventor/Applicant Name:	Bhanu KAPOOR						
Filer:	Kelly G. Hyndman/Mussie Beyene						
Attorney Docket Number:	Q82851						
Filed as Small Entity							
Utility Filing Fees							
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)		
Basic Filing:							
Pages:							
Claims:							
Miscellaneous-Filing:							
Petition:							
Patent-Appeals-and-Interference:							
Post-Allowance-and-Post-Issuance:							
Utility Appl issue fee		2501	1	720	720		
Publ. Fee- early, voluntary, or normal		1504	1	300	300		

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
	Total in USD (\$)			1020